

<b>Notice of References Cited</b>	Application/Control No. 10/008,195	Applicant(s)/Patent Under Reexamination BRIL ET AL.	
	Examiner Lewis A. Bullock, Jr.	Art Unit 2195	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Nilsen, Kelvin D. et al. "Worst-Case Execution Time Analysis on Modern Processors." ACM. November 1995.
	V	Jeffay, Kevin et al. "On Non-Preemptive Scheduling of Periodic and Sporadic Tasks." IEEE. December 1991.
	W	Lee, Chang-Gun et al. "Enhanced Analysis of Cache-related Preemption Delay in Fixed-priority Preemptive Scheduling." 1997.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.